

FIG. 1  
(PRIOR ART)

30	18
	12
	14
	24
	16

Figure 1A

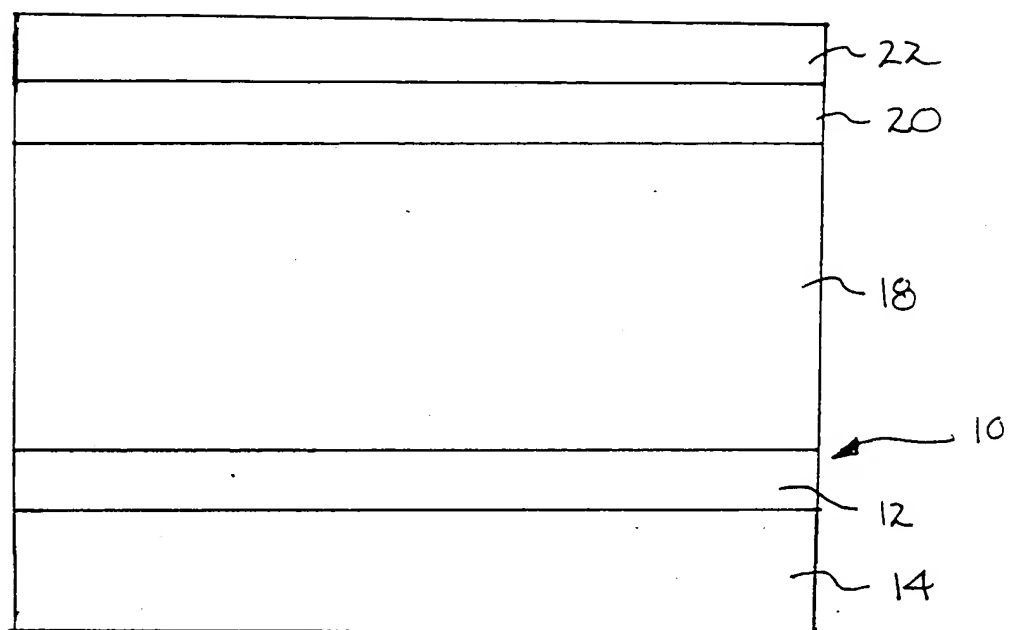


FIG. 2

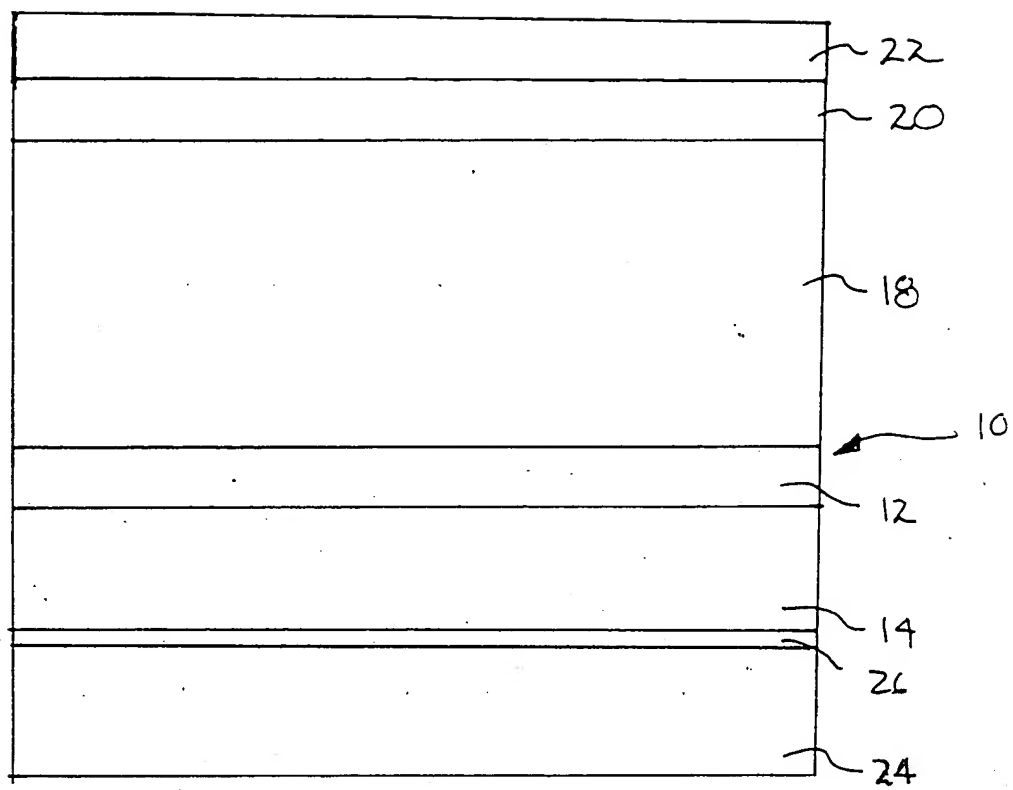


FIG. 3

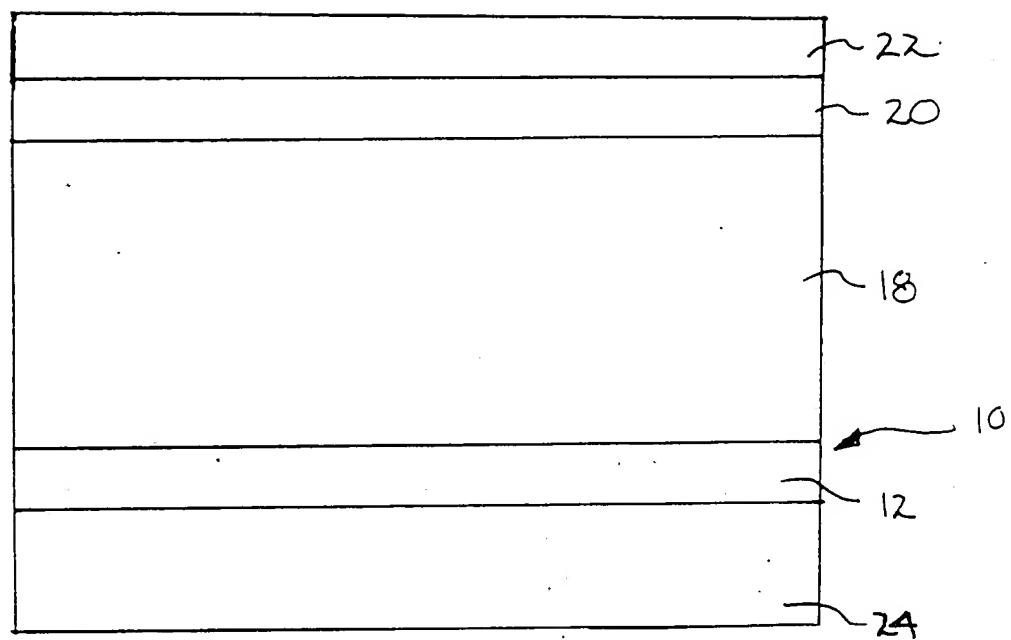


FIG. 4

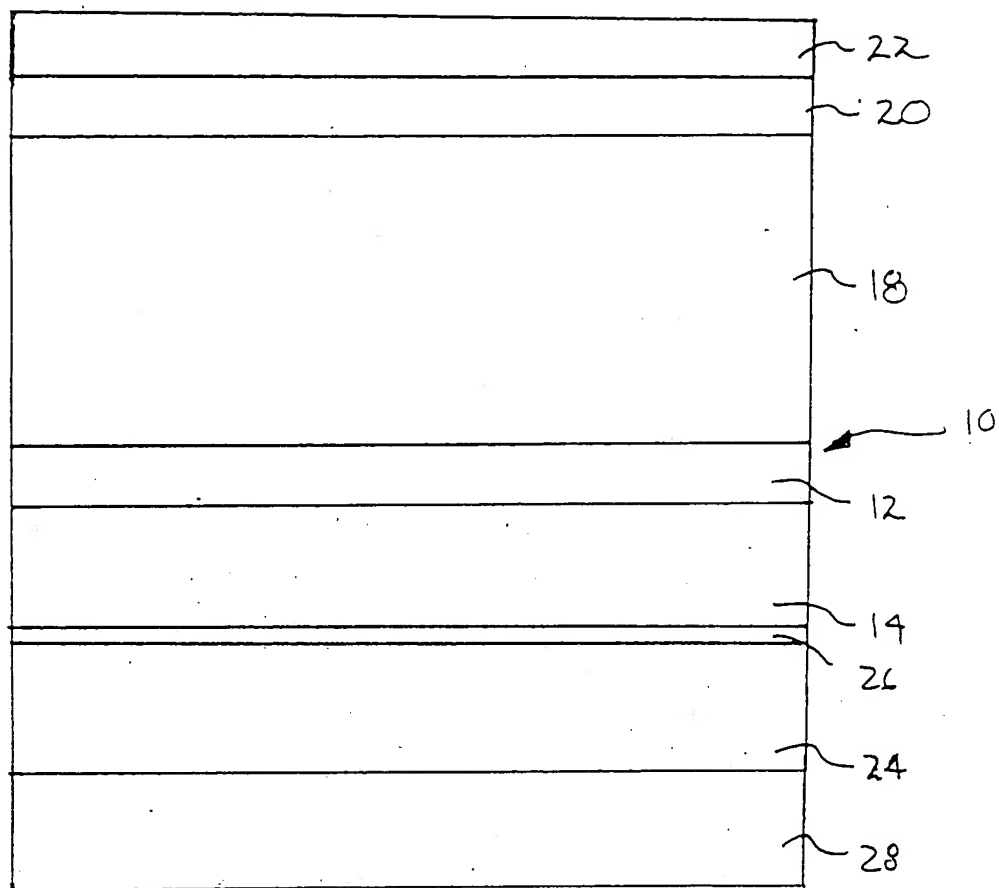


FIG. 5

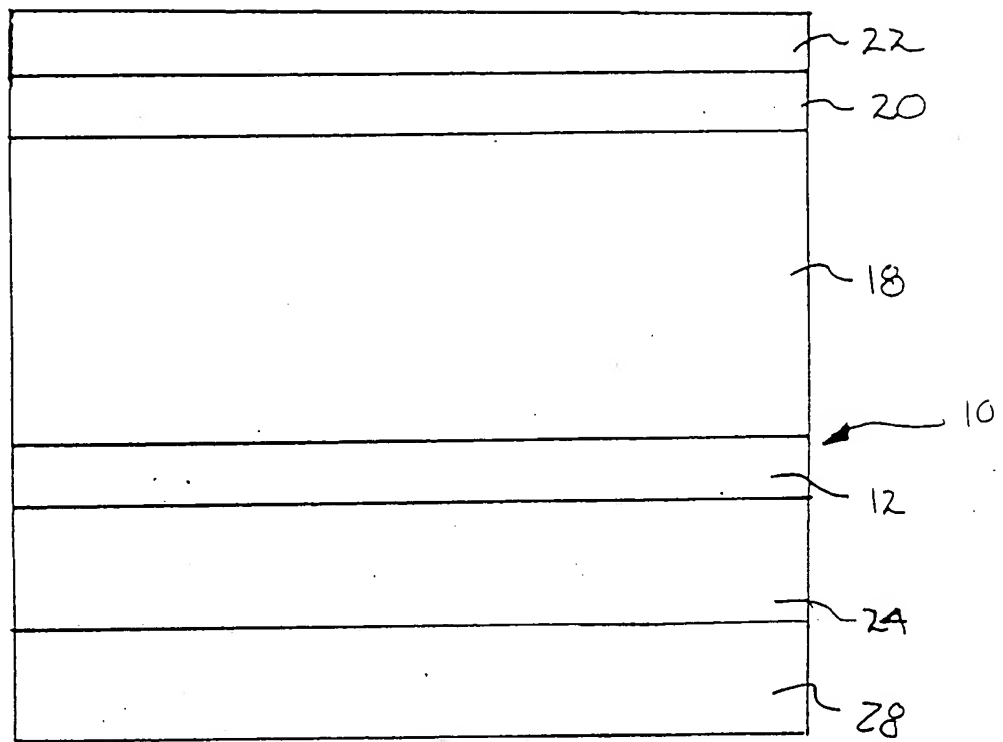


FIG. 6

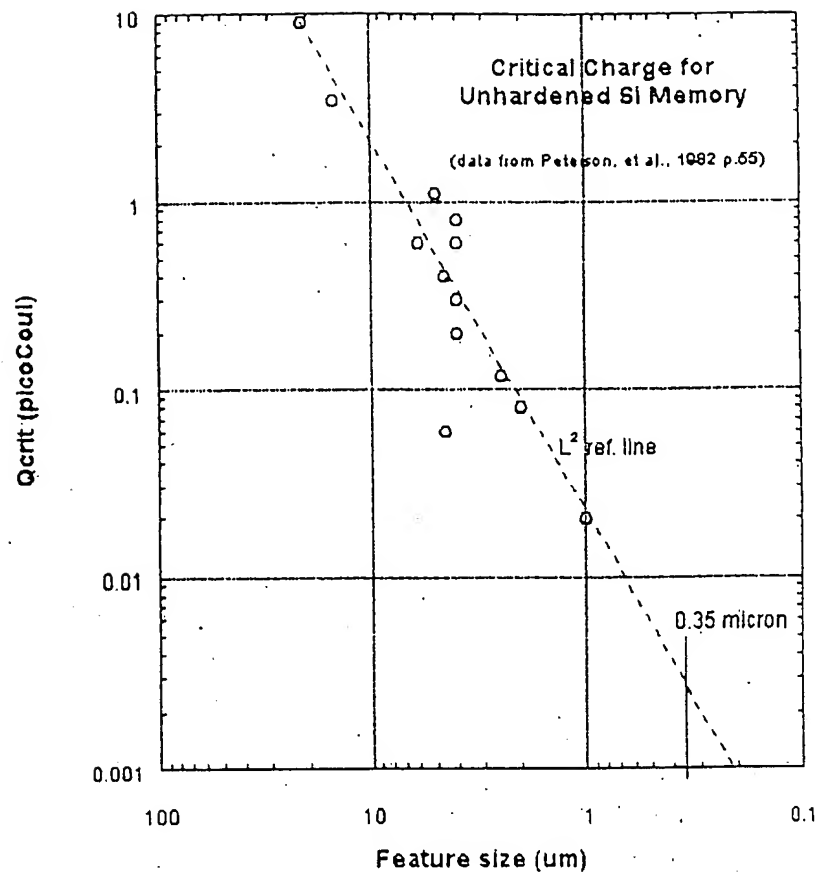


Fig. 7

## LET on Silicon

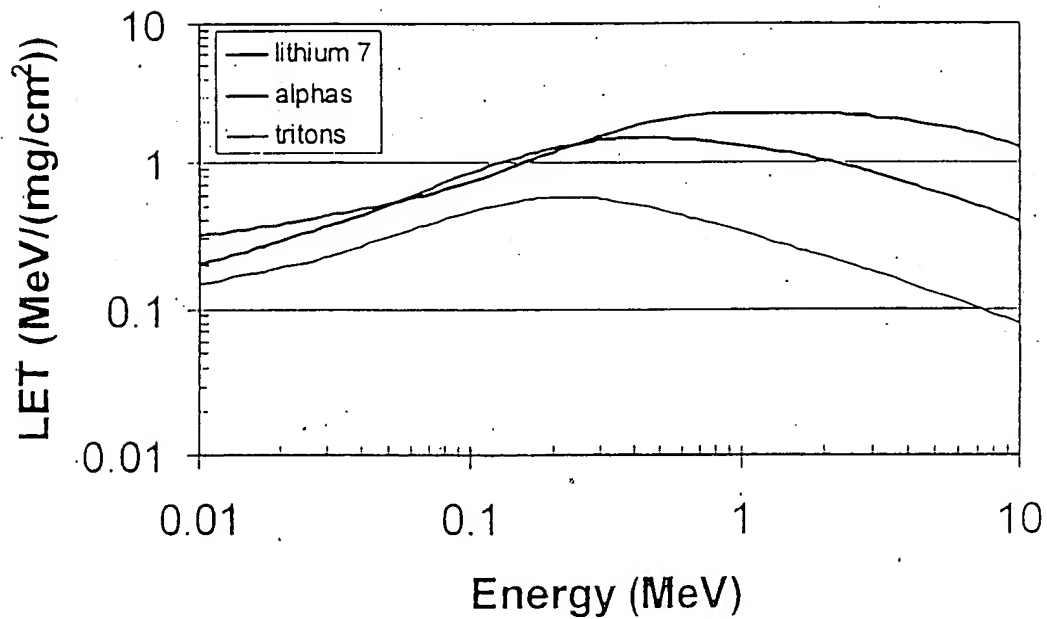


Fig. 8



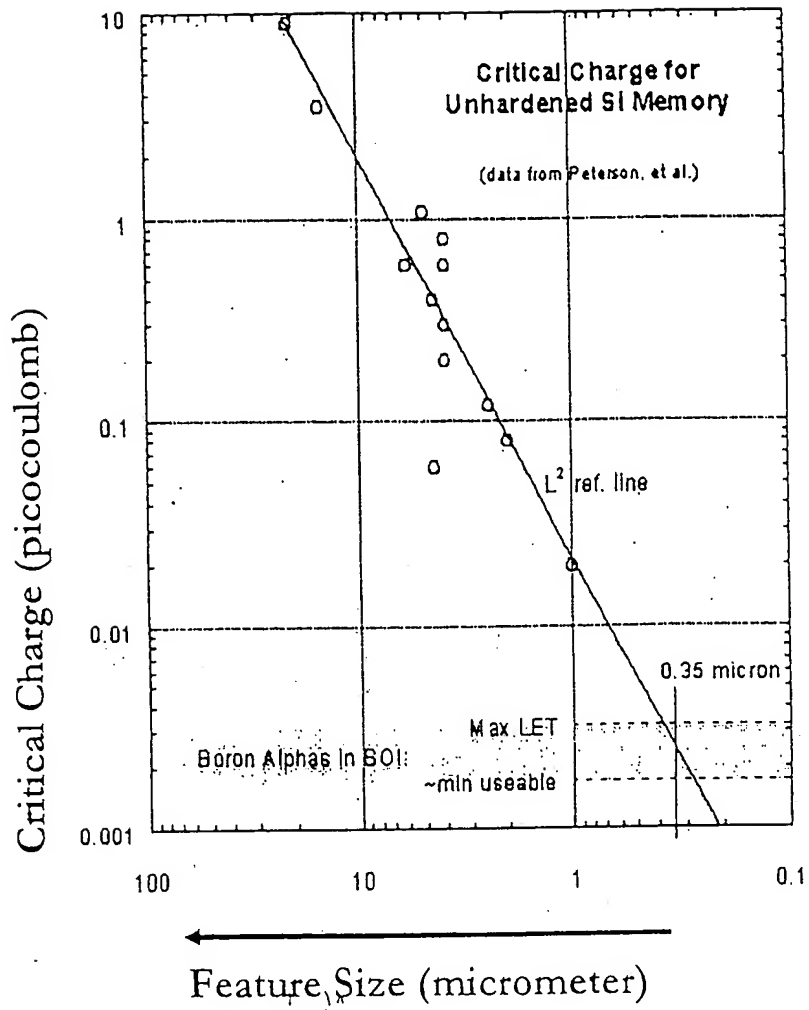


Fig 9